CAD-UDT Digital Circuit Fundamentals 2, FACET Board(40-91016-20) Product Fix: Digital Circuit Fundamentals 2 ver. 5.00.04 to 5.00.05

Digital Circuit Fundamentals 2 ver. 5.00.04 to 5.00.05

Unit 1:

ADC Operation: Procedure: Question 21: Updated feedback

Directions:

TechLab 6.0 & higher:

Download the file F50160010E.zip to your desktop. Then, extract F50160010E.tbk to the Tlsystem/unit \F50160010E directory on your TechLab server, overwriting the existing file.

Unit 2: Fundamentals: Revised graphic BDC Decoder Operation: Procedure: Question 7: Revised text Priority Encoder Operation: Procedure: Question 7: Revised text

Directions:

TechLab 6.0 & higher:

Download the file F50160020E.zip to your desktop. Then, extract F50160020E.tbk to the Tlsystem/unit \F50160020E directory on your TechLab server, overwriting the existing file.

Unit 3: ADC Operation: Procedure: Step 20: Text revision DAC Operation: Procedure: Step 9: Text revision

Directions:

TechLab 6.0 & higher:

Download the file F50160030E.zip to your desktop. Then, extract F50160030E.tbk to the Tlsystem/unit \F50160030E directory on your TechLab server, overwriting the existing file.

Unit 4: Unit Test: Revised graphic

Directions:

TechLab 6.0 & higher:

Download the file F50160040E.zip to your desktop. Then, extract F50160040E.tbk to the Tlsystem/unit \F50160040E directory on your TechLab server, overwriting the existing file.

Unit 5. Troubleshooting: Graphic revision 7-Segment LED Display: Question 3: Text and graphic revision

Directions:

TechLab 6.0 & higher:

Download the file F50160050E.zip to your desktop. Then, extract F50160050E.tbk to the Tlsystem/unit \F50160050E directory on your TechLab server, overwriting the existing file.

Course title: Revised version number

Directions:

TechLab 6.0 & higher:

Download the file F501600E.zip to your desktop. Then extract F501600E.gp2 to the tldata\gp2 directory on your TechLab server, overwriting the existing file.